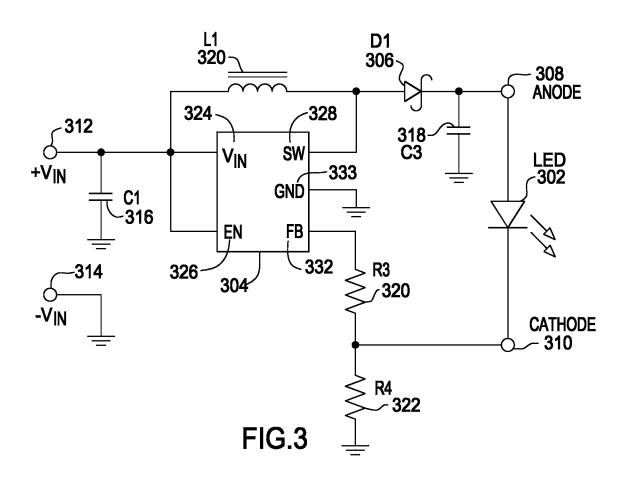
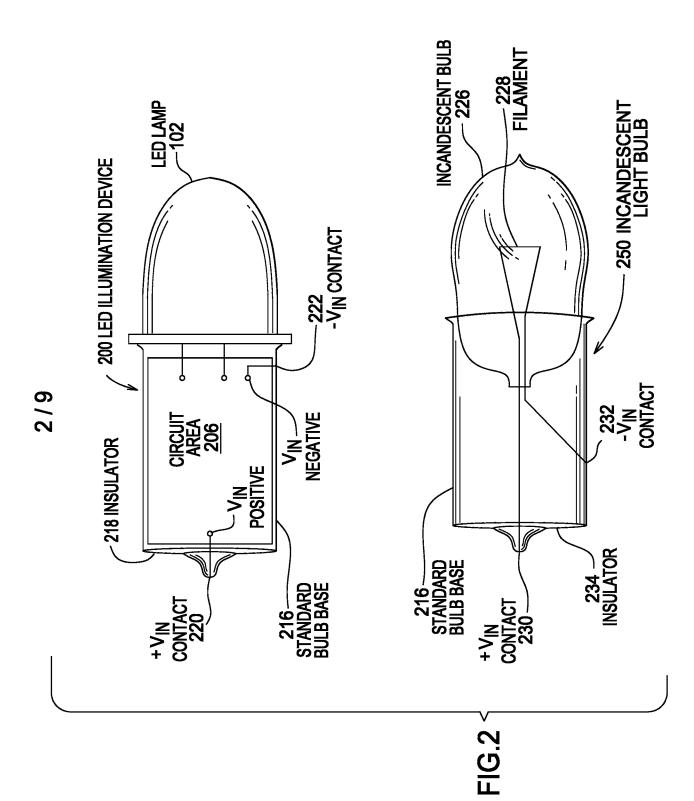


FIG.1







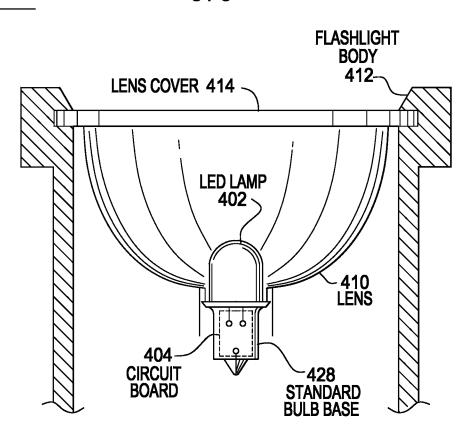
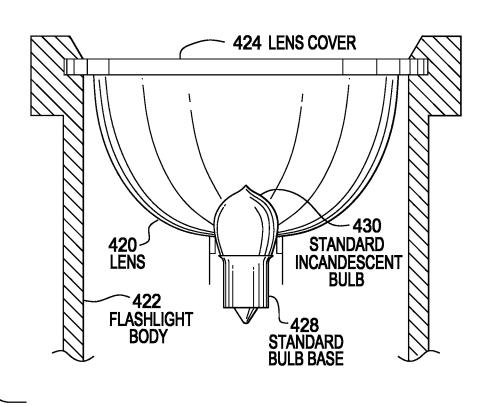


FIG.4



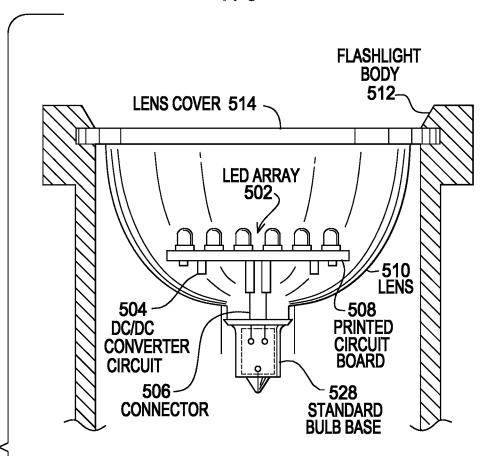
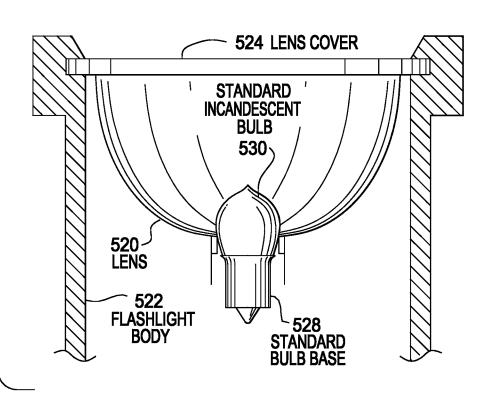
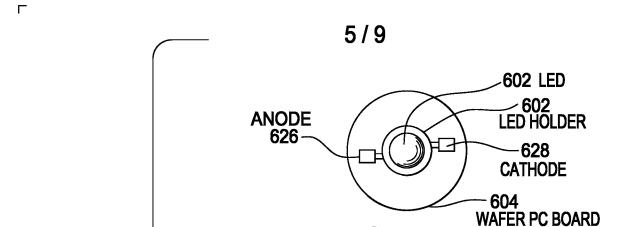


FIG.5



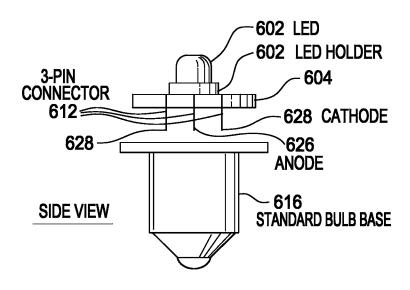


WAFER PC BOARD 606

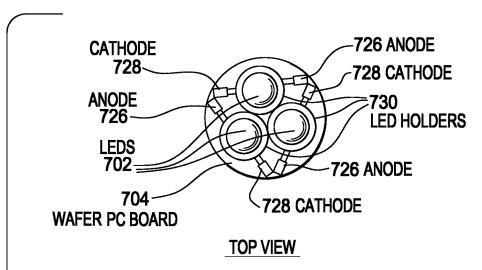
CONVERTER
AND LOGIC
CIRCUIT
BOTTOM VIEW

**TOP VIEW** 

FIG.6



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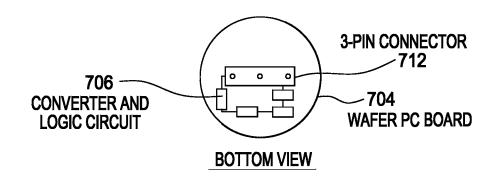
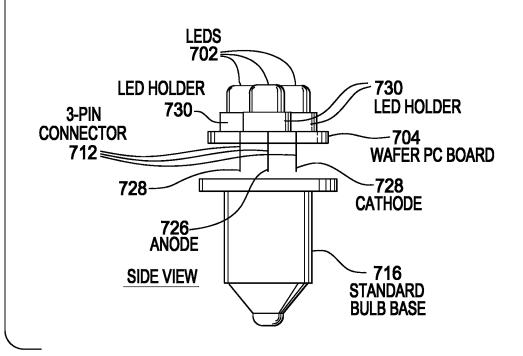
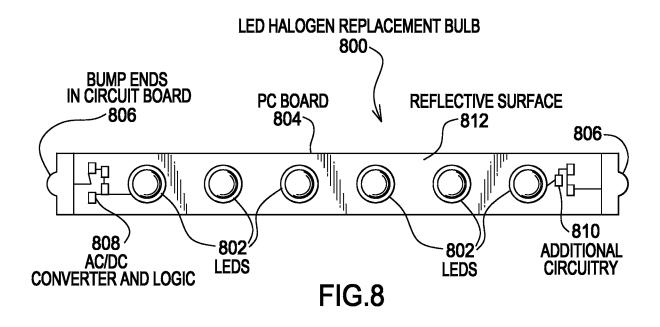


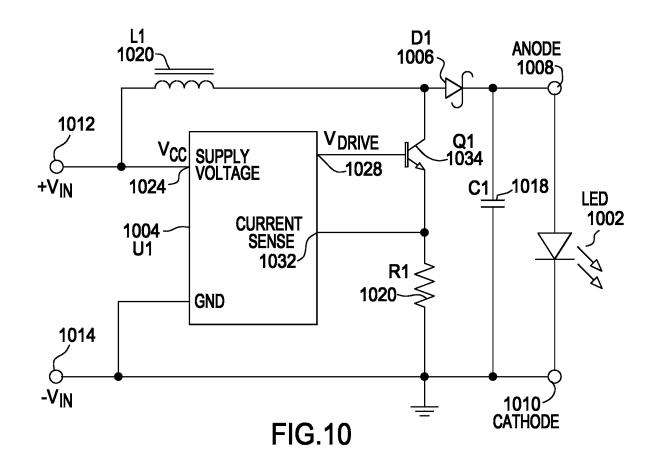
FIG.7

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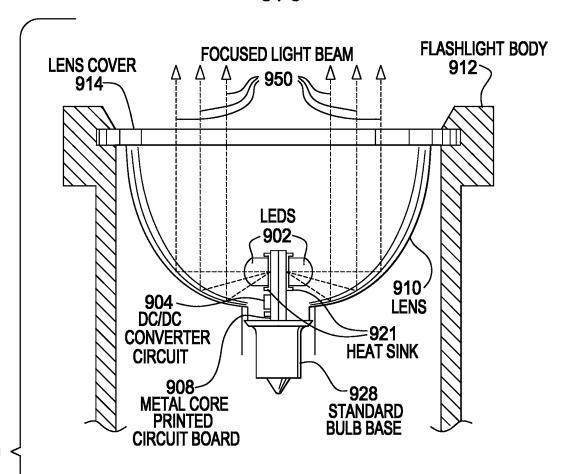
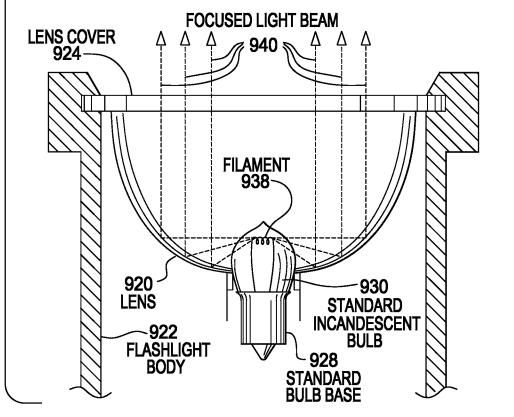


FIG.9

 $\Gamma$ 



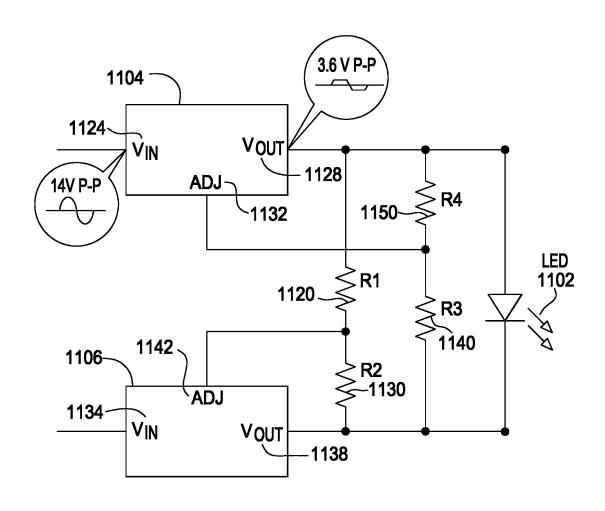


FIG.11